



- Can be used as a transmitter or receiver module
- Delivers precision clocks on TCLKA and TCLKB
- Provides triggers, gates, clocks or data on M-LVDS ports 17 - 20
- 3 RJ45 outputs on front with 2 triggers and one precision clock as LVDS signals
- Trigger position: 0 .. 160ms delay, 1ns resolution
- Trigger width: 0 .. 160ms, 10ns resolution
- Up to 255 trigger event numbers
- Precision clocks: 2.5 .. 650MHz
- Optional RTM with up to 9 additional triggers
- Optional RTM with up to 9 fiber links
- Clock and trigger jitter: ~10ps RMS
- Transmitter is implemented on a piggyback with 3 channels
- Transmitter has a micro controller with link length compensation to cancel temperature drifts of the fiber optical links

- Trigger with delay
- Immediate or delayed trigger events
- Gates between trigger events
- Slow clocks
- Two different slow data protocols
- Fast data protocol



