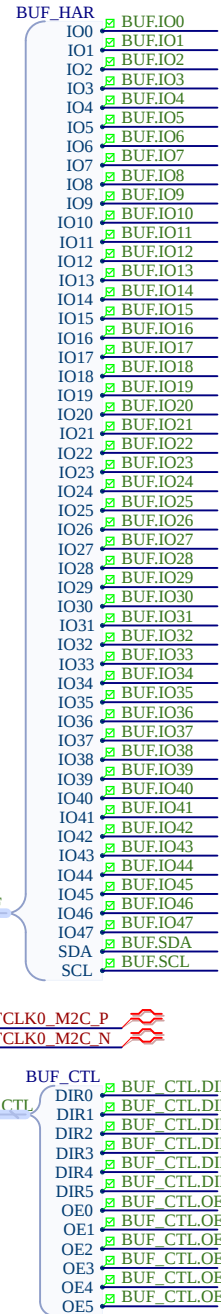
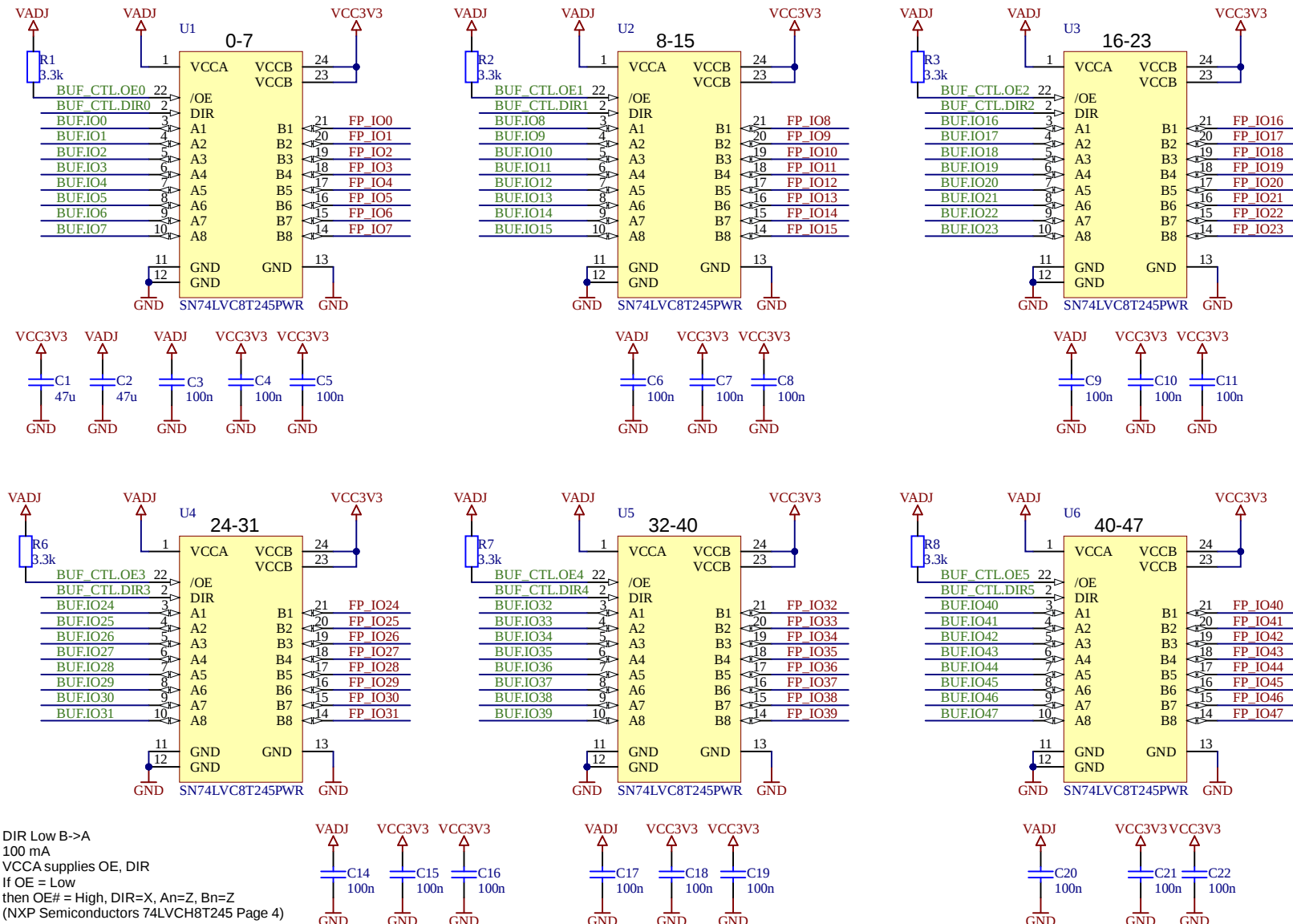


DFMC-SIO: Simple 48-channel (8bit x 6) IO adaptor

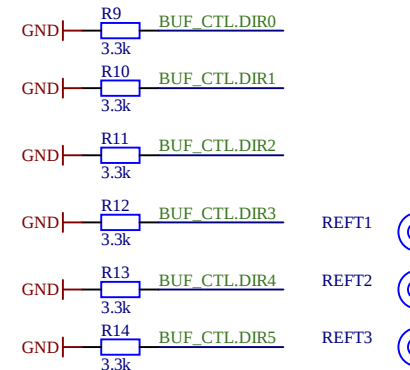
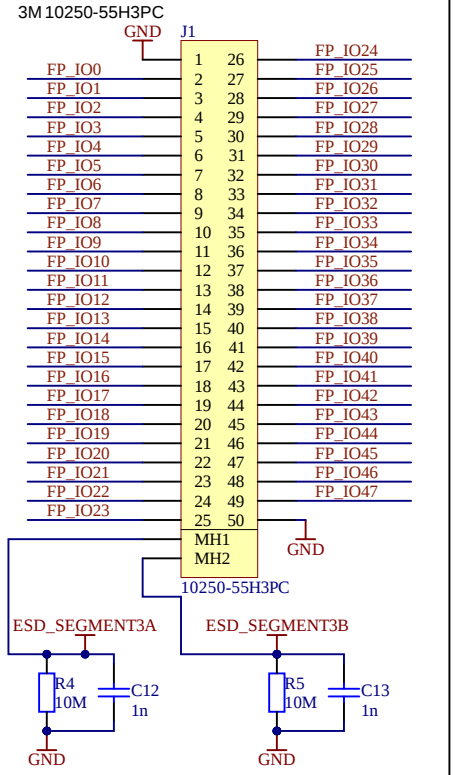
FMC connector



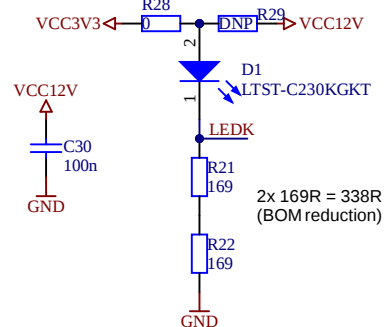
Bidirectional buffers



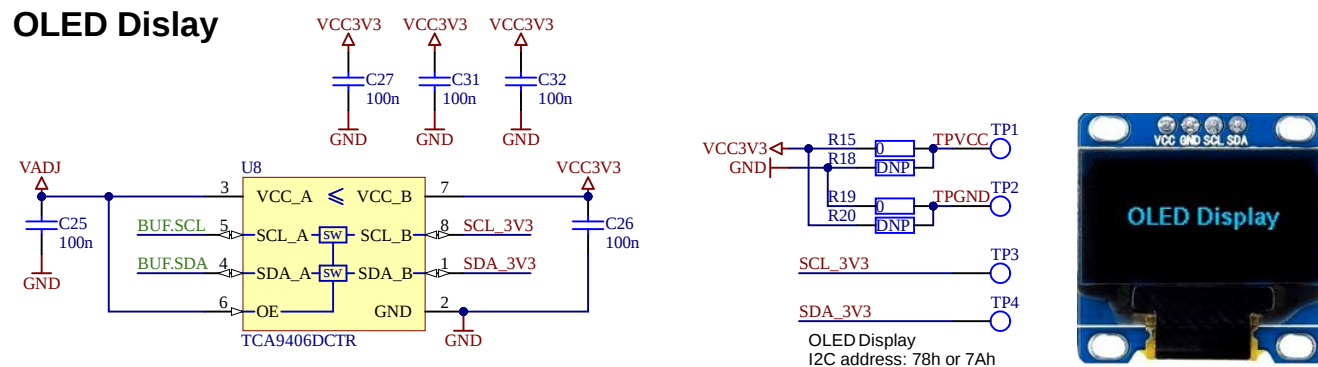
Mini-D Ribbon Connector



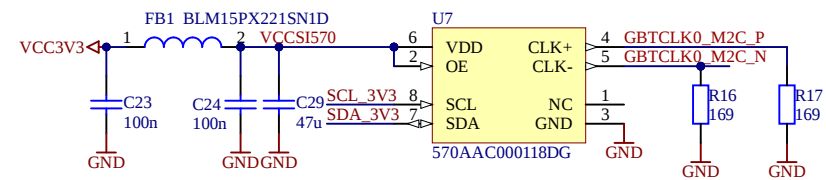
POWER LED



OLED Display



General-purpose MGT oscillator



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22607 Hamburg
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Project Title:
DMC-SIO.PrjPcb
Schematic Title:
Top Page

Engineer 1: Michael Fenner

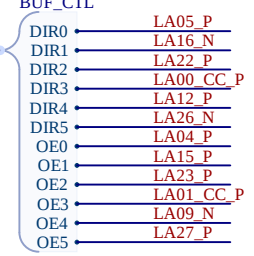
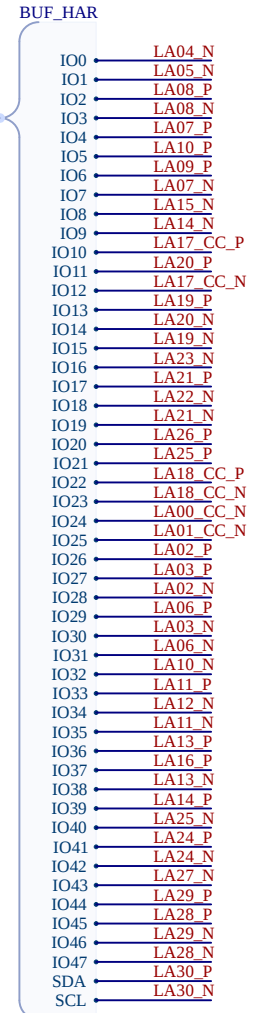
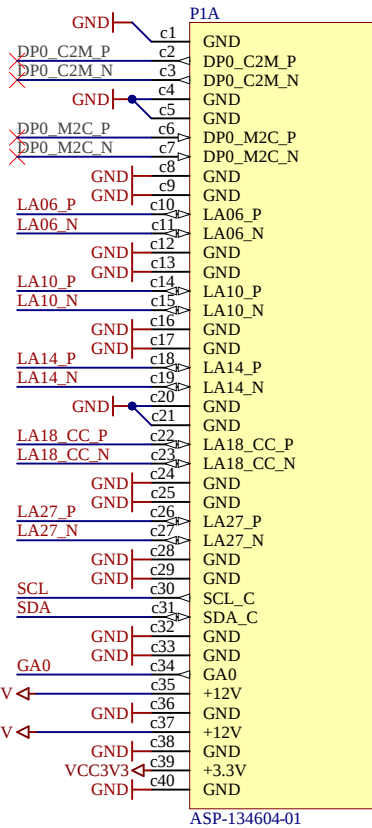
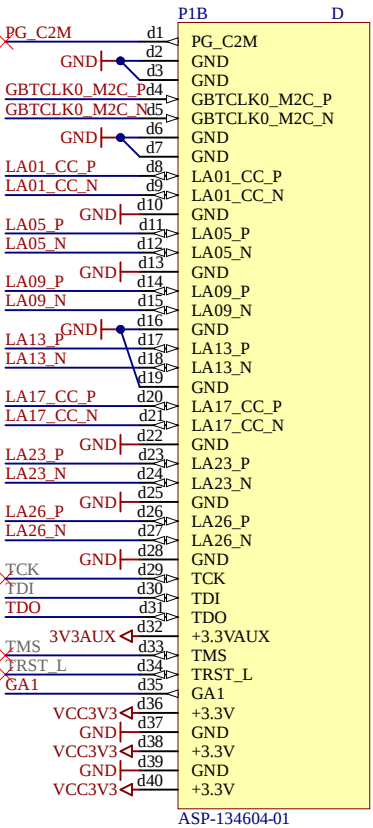
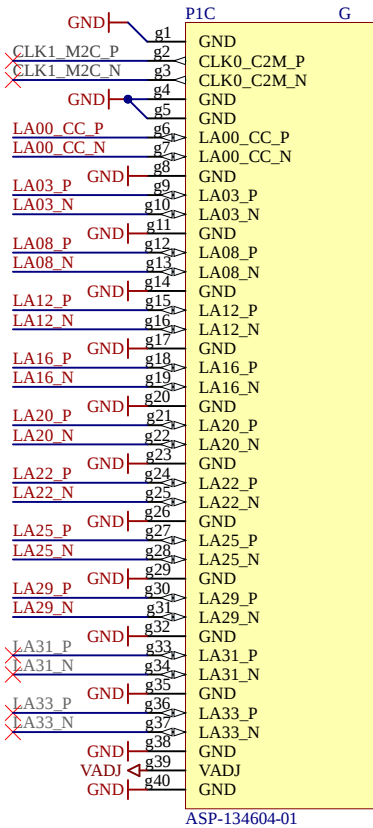
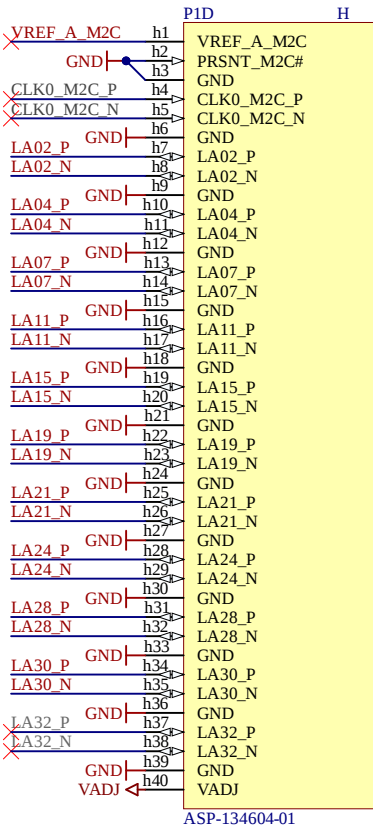
Engineer 2:



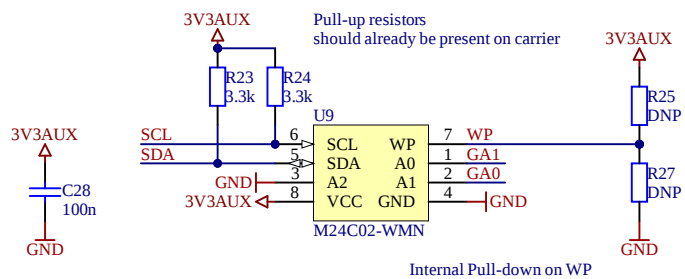
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FMC Breakout Rev. A

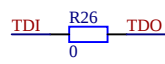
FMC LPC Connector



FMC EEPROM



JTAG bypass



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Project Title:
DMC-SIO.PrjPcb
Schematic Title:
FMC Connector
DATE: 27.02.2023 15:25:23



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Engineer 1: Michael Fenner
Engineer 2:

Rev: A
Size: A3
Sheet:

FILENAME: FMC_Conn.SchDoc

2 of 2